

ABSTRACT OF THE DISCLOSURE

A disclosed PLL can operate linearly at various frequencies by overlapping several VCOs and using a control circuit for controlling the several VCOs to select a VCO at a desired frequency. Accordingly, since the disclosed PLL can
5 automatically pre-compensate the frequency by using control signal used in the PLL, a separate frequency correcting signal is not required. When containing a VCO, the disclosed PLL can be configured into a single chip.